A method for resynchronizing a random clock on smart cards ...

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A short history of DPA attacks

- First published SPA DPA attack by Paul Kocher in 1998
- Silicon manufacturers introduce hardware countermeasures for all algorithms
- We focus on random clock



Plan

Introduce a way to reconstruct power curves

How does a DPA attack work

Manufacturer's random clock

A new method to reconstruct the signal



How a DPA attack works

• Principle : comparison of current consumption traces at the same instruction in the algorithm

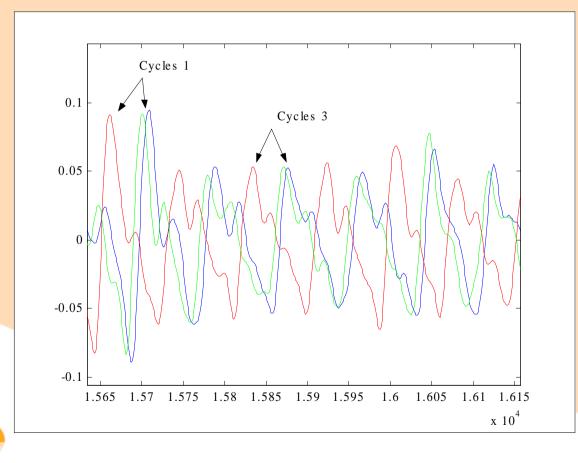
• Traces are superposed

 The necessary information is not diluted along the time scale : Easy to realise an attack



Manufacturer's random clock(1)

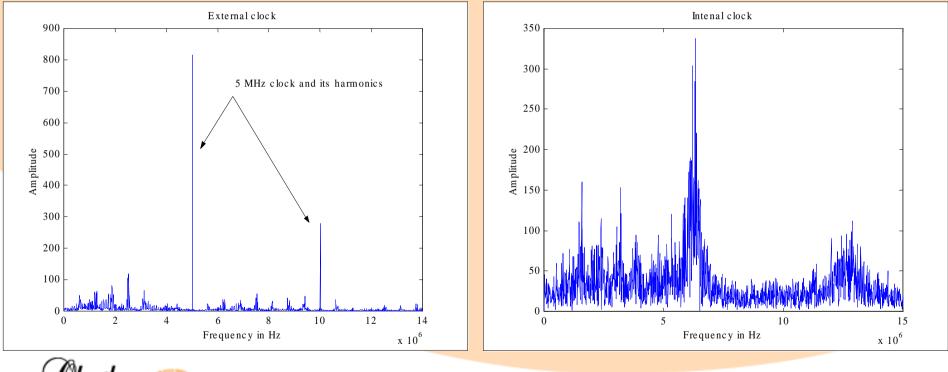
Time domain representation





Manufacturer's random clock(2)

FFT of "normal" and random clock





A new method to reconstruct the signal (1)

The protocol is the following :

Digital filtering of the traces
Find the number of cycles of the traces
Rebuild the curves where the same number of

minima has been found with two points per cycle

4) Conduct a DPA attack

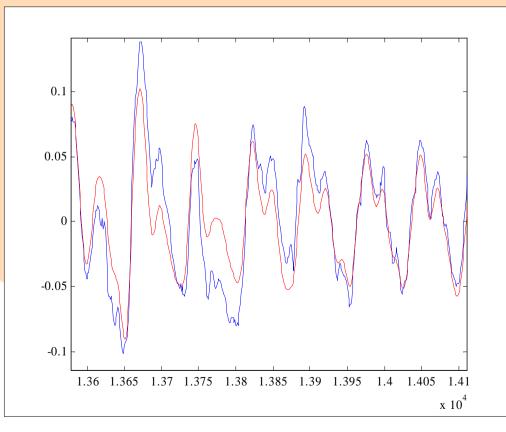


A new method to reconstruct the signal (2)

1) Digital filtering

In blue : original signal

In red : filtered signal





A new method to reconstruct the signal (3)

2) Find the number of cycles in the tracesRepeat for all traces

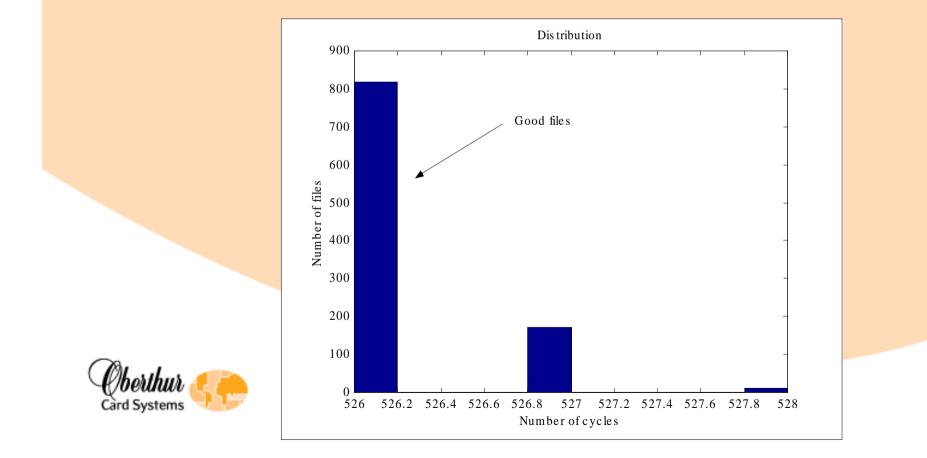
Repeat for all instructions

- Find a minima
- Look for the following minima in a given range



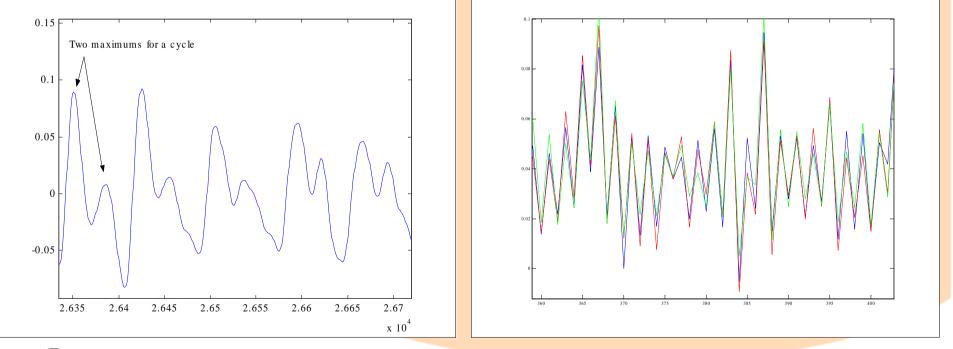
A new method to reconstruct the signal (4)

3) Distribution of the number of cycles



A new method to reconstruct the signal (5)

4) Each curve is reconstructed with two points per cycle





Results

•Results show the number of messages required to obtain 3 out of 4 selection functions giving the correct sub-key;

	S1	S2	S 3	S4
External clock	120	260	120	100
Random clock	1010	> 5000	> 5000	> 5000
Resynchronised clock	90	310	290	490



Conclusion (1)

- A method to reconstruct signals from a random clock for the component under test.
- It improves current DPA attacks without processing by a factor 10

In-depth study to improve these results is ongoing



Conclusion (2)

- Random clock countermeasures is good but not sufficient
- Software countermeasures also have to be implemented

